



Guangxin Jiang

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EDUCATION

Huazhong University of Science and Technology

Bachelor of Biomedical Engineering & Integrated Circuits Design

• GPA: 4.2/5 Rank: 6/54

• Key Courses: Digital IC (90); RF IC (96); Circuit Theory (97); Analog Electronic Technology (85); CMOS Analog IC (86); Signal and Linear System (96); Mathematical Physics Equation (97)

Wuhan, China

Sept. 2022 - Jun. 2026

The Hong Kong University of Science and Technology

Visiting Undergraduate of ECE supervised by Prof. [Zhiyong FAN](#)

Hong Kong SAR

Jul. 2025 – Aug. 2025

Westlake University

Visiting Undergraduate student supervised by Prof. [Mohamad sawan](#)

Hangzhou, China

Oct. 2025 – Present

PROJECT EXPERIENCE

Digital PET Imaging Lab IC Group

Oct. 2024-Jun.2025

Research Assistant

Advisor: [Qingguo Xie](#) (USTC & HUST)

- Reproduced a two-step Time-to-Digital Converter (TDC) circuit, where the first stage employs a single delay chain for coarse counting, and the second stage utilizes a vernier structure for fine counting.
- The circuit operates with a 100MHz clock signal and achieves time resolution of 20ps and a dynamic range of 10 ns. The basic delay units are controlled by the phase detector and charge pump which produce the locking voltage.
- Trying to add Time Amplifier in the measurement source and between the two-step TDC.

Machine learning-based early adolescent depression detection

Apr. 2024-Jul. 2024

Project Manager

Advisor: [Zhiwei Wang](#) (HUST WNLO)

- Based on the ECG and EEG multimodal data of patients with depression and healthy individuals, established a model which combines SVM and CNN-LSTM methods.
- Innovatively proposed the concept of "the proportion of depressive segments", which solved the problem of sample imbalance and effectively realized the screening and detection of early-onset depression in adolescents.

HONORS

National Scholarship (1/55)

2024.12

Merit Student of HUST (1/28)

2024.12

National Excellent Award of China Robotics and Artificial Intelligence Competition

2024. 08

Second Prize in National College Student Mathematics Competition

2024.01

National Encouragement Scholarship (1/28)

2023.12

SKILLS

Cadence Virtuoso: schematic and layout design, DRC, LVS, pre/post-layout simulation.

Software: C++, Python, MATLAB, LaTeX, LTspice, Proteus, Keil, Zemax, etc.

Language: Mandarin Chinese (Level 2B); English (CET-6: 511)

Certificate: NCRE Level3 (Network Technology).

ACTIVITY

Volunteer: Conducted a 15-day summer teaching program in Laifeng Hubei in 2023 and volunteered in optics valley marathon, third municipal hospital, Hankou railway station, summer enrollment, etc. (250 hours in total)

Student Affairs: Served as a member of the University Student Social Practice Center and Youth League Committee

ICAC Workshop 2025 Shenzhen China

A1 event of Half-Marathon (1:51:34 in Xiaogan, 2024 / 1:44:23 in Xiantao, 2025)